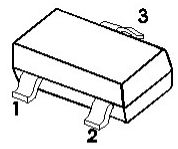
SOT-23 贴片塑封三极管

SOT-23 Plastic-Encapsulate Transistors

SOT-23



1. BASE
2. EMITTER
3. COLLECTOR

Marking: 1GM

特征 Features

- 适用于开关和放大运用 For Switching and Amplifier Applications
- 与 MMBTA56 配对; Complementary to MMBTA56
- 最大功率耗散 300mW; Power Dissipation of 300mW
- 高稳定性和可靠性。High Stability and High Reliability

机械数据 Mechanical Data

- 封装: SOT-23 封装 SOT-23 Small Outline Plastic Package
- 环氧树脂 UL 易燃等级 Epoxy UL: 94V-0
- 安装位置: 任意 Mounting Position: Any

极限值和温度特性(TA = 25℃ 除非另有规定)

Maximum Ratings & Thermal Characteristics (Ratings at 25℃ ambient temperature unless otherwise specified.)

参数 Parameters	符号 Symbol	数值 Value	单位 Unit
Collector-Base Voltage	V_{CBO}	80	V
Collector-Emitter Voltage	V_{CEO}	80	V
Emitter -Base Voltage	V_{EBO}	4	V
Collector Current-Continuous	I_C	500	mA
Collector Power Dissipation	P_C	300	mW
Junction Temperature	T_j	150	℃
Storage Temperature	Tstg	-55-+150	℃
Thermal resistance From junction to ambient	$R_{\theta JA}$	416	℃/W

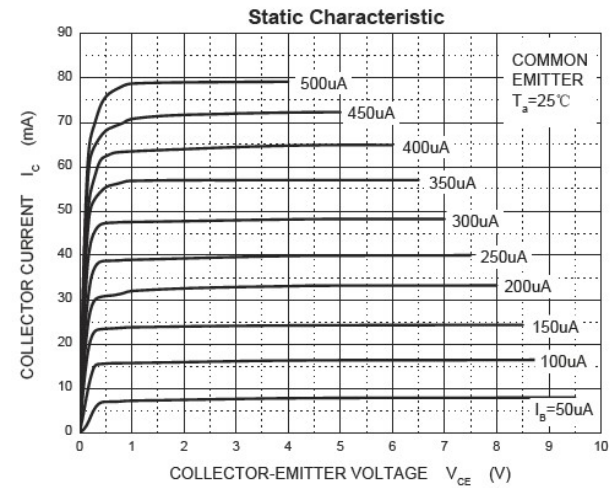
电特性 (TA = 25℃ 除非另有规定)

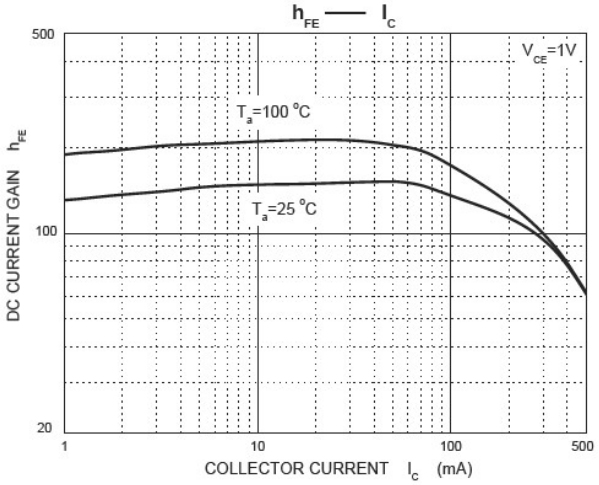
Electrical Characteristics (Ratings at 25℃ ambient temperature unless otherwise specified).

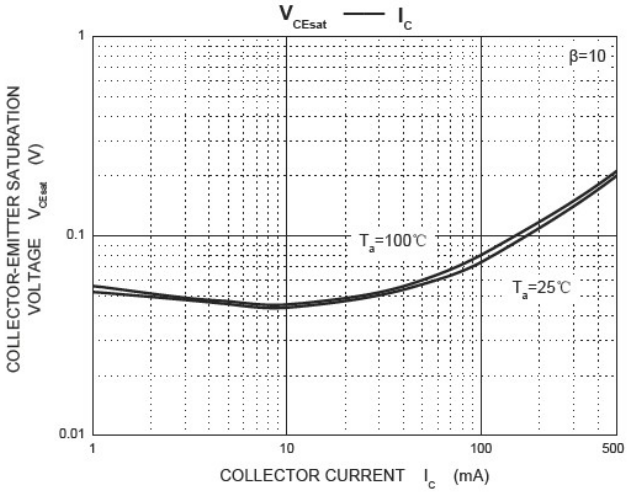
参数 Parameter	符号 Symbols	测试条件 Test Condition	界限 Limits		单位 Unit
			Min	Max	
Collector-base breakdown voltage	$V_{(BR)CBO}$	I_C=100uA, I_E=0	80		V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	I_C=1mA, I_B=0	80		V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	I_E=100uA, I_C=0	4		V
Collector cut-off current	I_{CBO}	V_{CB}=80V, I_E=0		100	nA
Collector cut-off current	I_{CEO}	V_{CE}=60V, I_B=0		1.0	uA
Emitter cut-off current	I_{EBO}	V_{EB}=3V, I_C=0		100	nA
DC current gain	$h_{FE(1)}$*	V_{CE}=1V, I_C=10mA	100	400	
	$h_{FE(2)}$*	V_{CE}=1V, I_C=100mA	100		
Collector-emitter saturation voltage	$V_{CE(sat)}$*	I_C=100mA, I_B=10mA		0.25	V
Base -emitter saturation voltage	$V_{BE(sat)}$*	I_C=100mA, I_B=10mA		1.20	V
Transition frequency	f_T	V_{CE}=2V, I_C=10mA,f=100MHz	300		MHz

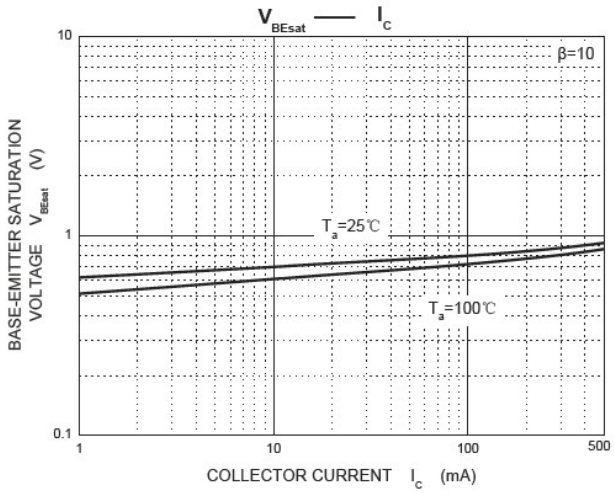
*Pulse test: pulse width≤300us, duty cycle≤2.0%.

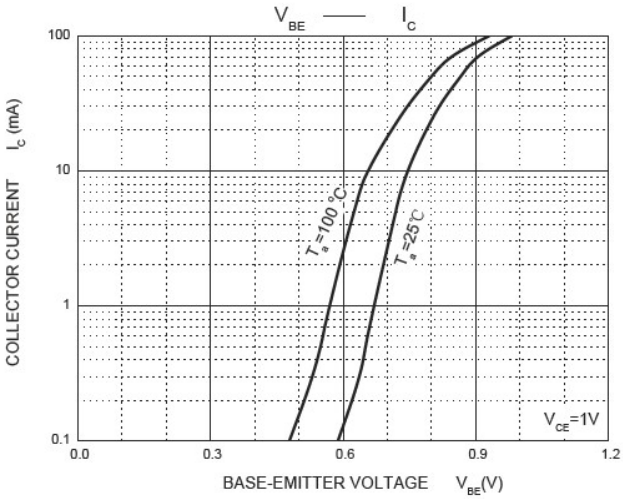
Typical characteristics

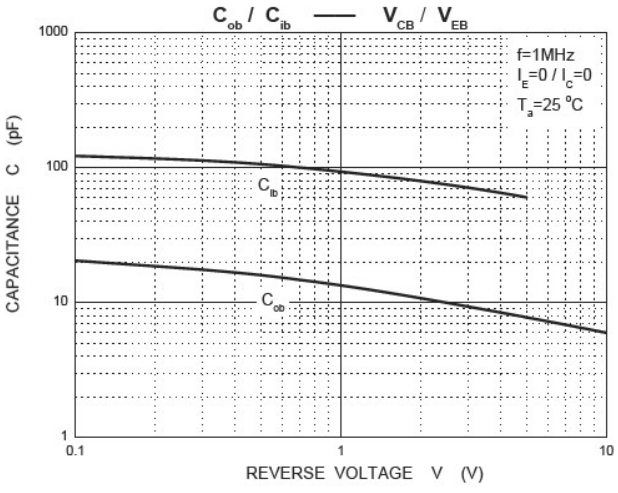


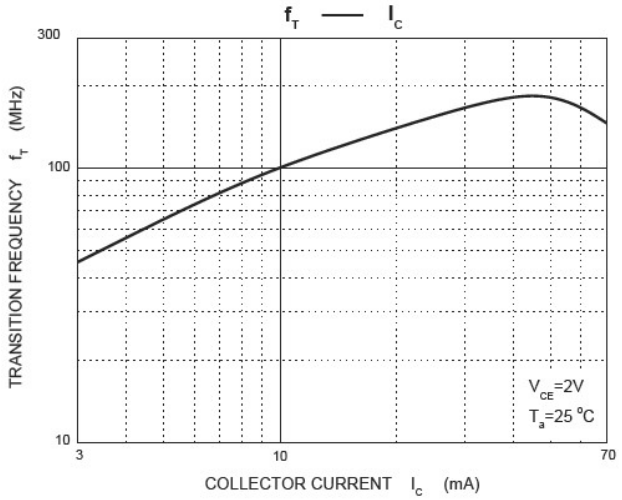


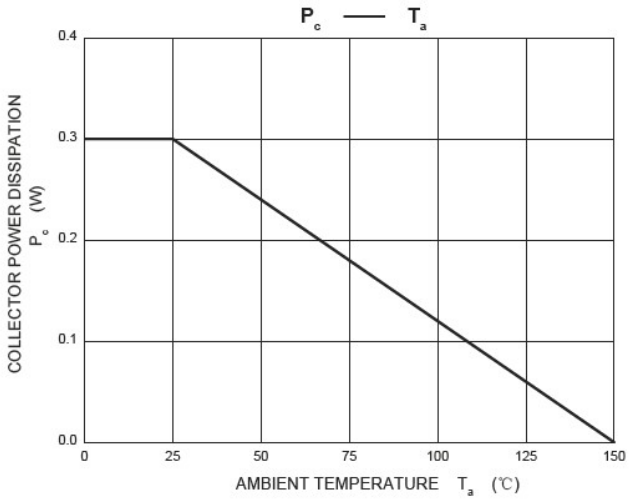




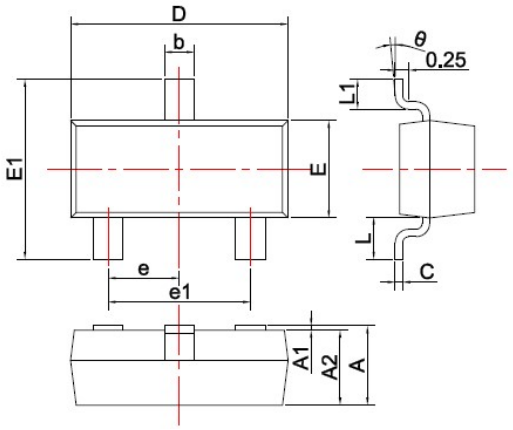








SOT-23 PACKAGE OUTLINE **Plastic surface mounted package**

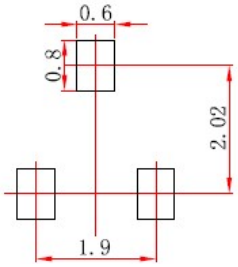


SYMBOL	DIMENSIONS	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

Unit: mm

焊盘设计参考Precautions: PCB Design

Recommended land dimensions for SOT-23 diode. Electrode patterns for PCBs



Note:
1.Controlling dimension:In millimeters.
2.General tolerance:± 0.05mm.
3.The pad layout is for reference purposes only.